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EXAMINER

BAKER, S

ART UNIT

2306

PAPER NUMBER

15

DATE MAILED: 01/16/92

ANTONELLI, TERRY & WANDS
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WASHINGTON, DC 20006

This is a communication from the examiner in charge of your application.
COMMISSIONER OF PATENTS AND TRADEMARKS

☐ This application has been examined ☒ Responsive to communication filed on 9/26/91 ☒ This action is made final.

A shortened statutory period for response to this action is set to expire 3 month(s), — days from the date of this letter.
Failure to respond within the period for response will cause the application to become abandoned. 35 U.S.C. 133

Part I THE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:

- ☒ Notice of References Cited by Examiner, PTO-892.
- ☐ Notice re Patent Drawing, PTO-948.
- ☐ Notice of Art Cited by Applicant, PTO-1449.
- ☐ Notice of Informal Patent Application, Form PTO-152.
- ☐ Information on How to Effect Drawing Changes, PTO-1474.
- ☐

Part II SUMMARY OF ACTION

- ☒ Claims 7, 9-13 and 15-73 are pending in the application.
Of the above, claims _____ are withdrawn from consideration.
- ☐ Claims _____ have been cancelled.
- ☐ Claims _____ are allowed.
- ☒ Claims 7, 9-13 and 15-73 are rejected.
- ☐ Claims _____ are objected to.
- ☐ Claims _____ are subject to restriction or election requirement.
- ☐ This application has been filed with informal drawings under 37 C.F.R. 1.85 which are acceptable for examination purposes.
- ☐ Formal drawings are required in response to this Office action.
- ☐ The corrected or substitute drawings have been received on _____. Under 37 C.F.R. 1.84 these drawings are ☐ acceptable. ☐ not acceptable (see explanation or Notice re Patent Drawing, PTO-948).
- ☐ The proposed additional or substitute sheet(s) of drawings, filed on _____ has (have) been ☐ approved by the examiner. ☐ disapproved by the examiner (see explanation).
- ☐ The proposed drawing correction, filed on _____, has been ☐ approved. ☐ disapproved (see explanation).
- ☐ Acknowledgment is made of the claim for priority under U.S.C. 119. The certified copy has ☐ been received ☐ not been received
☐ been filed in parent application, serial no. _____; filed on _____.
- ☐ Since this application appears to be in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11; 453 O.G. 213.
- ☐ Other

1. The disclosure is objected to because of the following informalities: In the amendment to page 32 of the specification, at lines 11-14, "The change of load capacitance" should apparently read as "The charge of load capacitance". Appropriate correction is required.

2. Claims 23, 32, 33, 50-62 and 71-73 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 23 recites an "external supply voltage" receiving an "external supply voltage". In claims 32 and 33, "an gate" should read as "a gate". In claims 50, 51, 59 and 60, "said first, second and third transistors" lack an antecedent basis. In claims 61 and 62, "said first transistor" lacks an antecedent basis. In claims 71-73, "change" should apparently read as "charge".

3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

4. Claims 7, 9-13 and 15-73 are rejected under 35 U.S.C. § 102(b) as being anticipated by Alaspa.

Alaspa discloses an on-chip automatic power-on reset circuit comprising transistors 44 and 48 which provides an "internal power supply" to the gates of transistors to be connected to receive an "internal supply" reset voltage V_R . Fig. 3 shows V_R

as a function of an "external supply" V_{DD} . A manual reset input 54 controls the "driving ability" of the reset circuit by grounding V_R .

Regarding claims 21, 34-42, 63, 64, 69 and 70, transistor 28, 30, or 56 is a "first transistor" that would have a higher breakdown voltage than a transistor having conventional dimensions of 0.4/0.4 in a "second circuit" that would respond to V_R .

Regarding claims 26-70, a diode 20 resistance and a "first (or third) transistor" 22 are a "reference voltage generating means". The circuits to be reset are of course latches that provide "memory cells".

Regarding claims 71-73, a gate signal charges a gate-to-channel capacitance.

5. Claims 13, 15, 20-24, 26, 35, 44 and 53 are rejected under 35 U.S.C. § 102(b) as being anticipated by Takanishi et al.

Takanishi et al. disclose an IC with an operation voltage supply transistor M^* having a larger breakdown voltage than the load transistors 2 so that the load transistors may be made smaller while maintaining breakdown resistance. M^* is an "internal power supply means" connected to an "external supply voltage" V_D and receiving a "control signal" ground voltage generated at its gate that controls the "driving ability" which is the channel current. The load circuits 2 shown in Fig. 3 are

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"internal" "second circuits" and the display drivers M*32 and M*33 are "interface" "first circuits" also having higher breakdown voltages than the load transistors.

Regarding claim 15, the drain of M* detects a change in V_D .

Regarding claims 26, 35, 44 and 53, the conductor connected to the gate of M* has a resistance.

6. Applicant's arguments filed 7/19/91 have been fully considered but they are not deemed to be persuasive.

7. Applicant's amendment necessitated the new grounds of rejection. Accordingly, **THIS ACTION IS MADE FINAL**. See M.P.E.P. § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 C.F.R. § 1.136(a).

A SHORTENED STATUTORY PERIOD FOR RESPONSE TO THIS FINAL ACTION IS SET TO EXPIRE THREE MONTHS FROM THE DATE OF THIS ACTION. IN THE EVENT A FIRST RESPONSE IS FILED WITHIN TWO MONTHS OF THE MAILING DATE OF THIS FINAL ACTION AND THE ADVISORY ACTION IS NOT MAILED UNTIL AFTER THE END OF THE THREE-MONTH SHORTENED STATUTORY PERIOD, THEN THE SHORTENED STATUTORY PERIOD WILL EXPIRE ON THE DATE THE ADVISORY ACTION IS MAILED, AND ANY EXTENSION FEE PURSUANT TO 37 C.F.R. § 1.136(a) WILL BE CALCULATED FROM THE MAILING DATE OF THE ADVISORY ACTION. IN NO EVENT WILL THE STATUTORY PERIOD FOR RESPONSE EXPIRE LATER THAN SIX MONTHS FROM THE DATE OF THIS FINAL ACTION.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen Baker whose telephone number is (703) 308-0850.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 308-0754.

SMB
January 15, 1992



STEPHEN M. BAKER